ABSTRACT OF THE DISCLOSURE

A signal processor receives an encoded digital signal and generates signals to reproduce video and audio from the received digital signal. The signal processor includes: a first decoder for separating a first video signal and a first audio signal in digital form from a digital signal in a first format; a second decoder for separating a second video signal and a second audio signal in digital form from a digital signal in a second format; a video controller, which receives the first and second video signals, selects at least one of the two signals received, subjects the selected signal to video processing for display purposes, and then outputs the processed signal; a clock generator for generating a clock signal of which the frequency corresponds to that of the first audio signal; an audio processor, which receives the second audio signal and the clock signal and converts the frequency of the second audio signal into that of the first audio signal in accordance with the clock signal; and an audio switch, which receives the first audio signal from the first decoder and the second audio signal with the converted frequency from the audio processor, respectively, and outputs one of the two audio signals that is associated with the video signal being selected by the video controller.